



Shri Vile Parle Kelavani Mandal's

**DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING**

(Autonomous College Affiliated to the University of Mumbai)

NAAC Accredited with "A" Grade (CGPA : 3.18)



**END SEMESTER EXAMINATION TIMETABLE, June, 2025**  
**BACHELOR OF TECHNOLOGY (B. Tech.) All Branches**  
**FIRST YEAR, SEMESTER – II (For 2024-25 Batch Under Autonomy)**

Date: 24<sup>th</sup> May, 2025

**Course Name: Engineering Graphics**

Day & Date	DIV / Batch	Roll No.		Time	Venue
		From	To		
02-06-2025	A1 (All Batches)	A001	A063	8.30 AM to 10.30 AM	Advance Computer Lab (2nd floor)
	A2 (All Batches)	A064	A127	11.30 AM to 01.30 PM	
	B (All Batches)	B001	B064	2.30 PM to 04.30 PM	
	S (S1 Batch)	S001	S021	9.00 AM to 11.00 AM	Engineering Graphics CAD lab (2nd floor)
	S (S2 Batch)	S022	S042	12.00 PM to 02.00 PM	
	S (S3 Batch)	S043	S064	3.00 PM to 05.00 PM	
03-06-2025	E1 (All Batches)	E001	E061	8.30 AM to 10.30 AM	Advance Computer Lab (2nd floor)
	E2 (All Batches)	E062	E123	11.30 AM to 01.30 PM	
	E3 (All Batches)	E124	E185	2.30 PM to 04.30 PM	
04-06-2025	M1 (All Batches)	M001	M062	8.30 AM to 10.30 AM	Advance Computer Lab (2nd floor)
	M2 (All Batches)	M063	M124	11.30 AM to 01.30 PM	
	M3 (All Batches)	M125	M187	2.30 PM to 04.30 PM	

All the students are hereby informed to report at least **15 minutes** prior to the scheduled time in **respective laboratories**.

**Important Note:** Changes if any, in the timetable, shall be communicated on the College website (djsce.ac.in).

Ms. Alka Shukla  
Controller of Examinations

Dr. Hari Vasudevan  
Principal